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Model Name: T315HW07 V7

Issue Date : 2010/11/30

( ) Preliminary Specifications
(*) Final Specifications

Customer Signature	Date	AUO	Date
Approved By		Approval By PM Director Yen Ting Chiu 2010/12/10	
Note		Reviewed By RD Director Eugene CC Chen 2010 Reviewed By Project Leader HS Huang 12/3	
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Record of Revision

Version	Date	Page	Description
0.0	2010/10/20		First release
0.1	2010/11/30		Final Spec
	2010/12/13	26	Update packing drawing
	2011/01/19	17	Update Minimum and Maximum Forward Voltage
	2011/01/31	13	Update Signal Timing
	2011/01/31	16	Update LCD Power Sequence

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1. General Description

This specification applies to the 32.0 inch Color TFT-LCD Module T315HW07 V7. This LCD module has a TFT active matrix type liquid crystal panel 1,920x1,080 pixels, and diagonal size of 32.0 inch. This module supports 1,920x1,080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot.

The T315HW07 V7 has been designed to apply the 10-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

*** General Information**

Items	Specification	Unit	Note
Active Screen Size	32.00	inch	
Display Area	698.40(H) x 392.85(V)	mm	
Outline Dimension	733.6(H) x 432.0 (V) x 25.1(D)	mm	D: front bezel to T-con cover
Driver Element	a-Si TFT active matrix		
Bezel Opening	703.6(H) x 399(V)	mm	
Display Colors	10 bit, 1073.7M	Colors	
Number of Pixels	1,920x1,080	Pixel	
Pixel Pitch	0.36375 (H) x 0.36375 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=2%

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2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

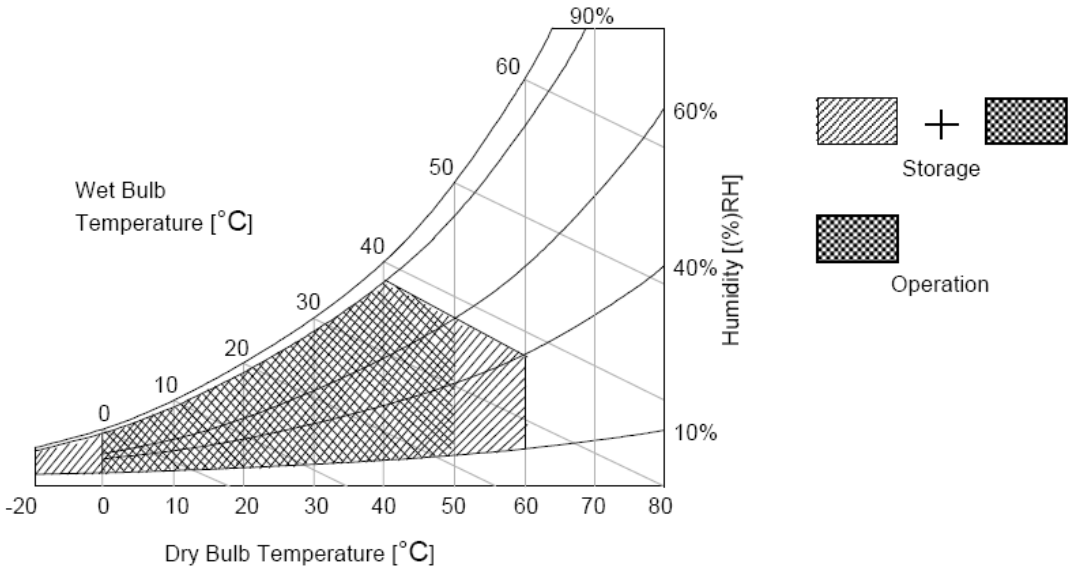
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39℃ and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.

Note 3: Surface temperature is measured at 50℃ Dry condition



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3. Electrical Specification

The T315HW07 V7 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other is to power Back Light Unit.

3.1 Electrical Characteristics

3.1.1: DC Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LCD							
Power Supply Input Voltage		V_{DD}	10.8	12	13.2	V_{DC}	
Power Supply Input Current		I_{DD}	-	0.47	0.98	A	1
Power Consumption		P_C	--	5.64	11.76	W	1
Inrush Current		I_{RUSH}	--	--	6	A	2
LVDS Interface	Input Differential Voltage	$\lvert V_{ID} \rvert$	200	400	600	mV_{DC}	3
	Differential Input High Threshold Voltage	V_{TH}	+100	--	+300	mV_{DC}	3
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV_{DC}	3
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V_{DC}	3
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.7	--	3.3	V_{DC}	4
	Input Low Threshold Voltage	V_{IL} (Low)	0	--	0.6	V_{DC}	4
Life time (MTTF)			30000			Hour	7, 8

3.1.2: AC Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LVDS Interface	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%	--	Fclk +3%	MHz	5
	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30	--	200	KHz	5
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	 -0.4 -0.5	 -- --	 0.4 0.5	ns	6

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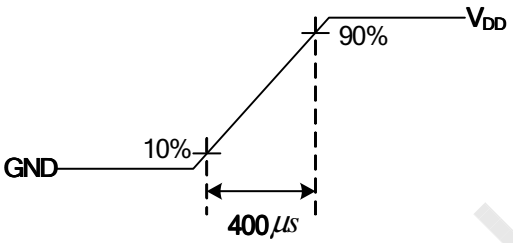
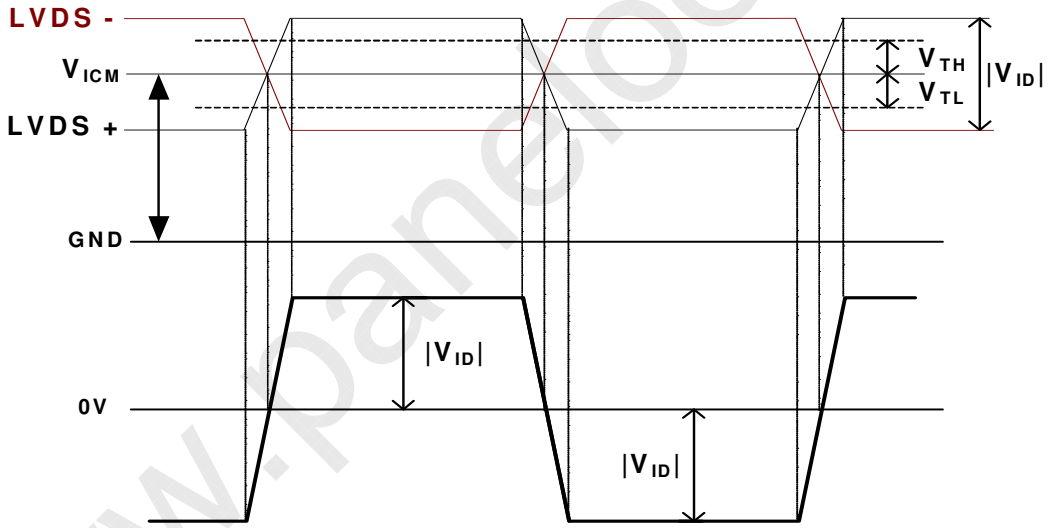
Note :

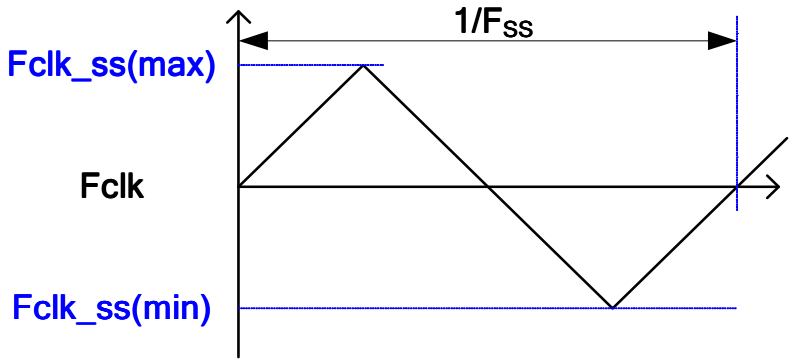
1. Test Condition:
   (1) V_{DD} = 12.0V
   (2) Fv = Type Timing, 120Hz
   (3) Fclk= Max freq.
   (4) Temperature = 25 ℃
   (5) Typ. Input current : White Pattern
   Max. Input current: Heavy loading pattern defined by AUO
2. Measurement condition : Rising time = 400us



3. V_{ICM} = 1.25V



4. The measure points of V_{IH} and V_{IL} are in LCM side after connecting the System Board and LCM.
5. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures



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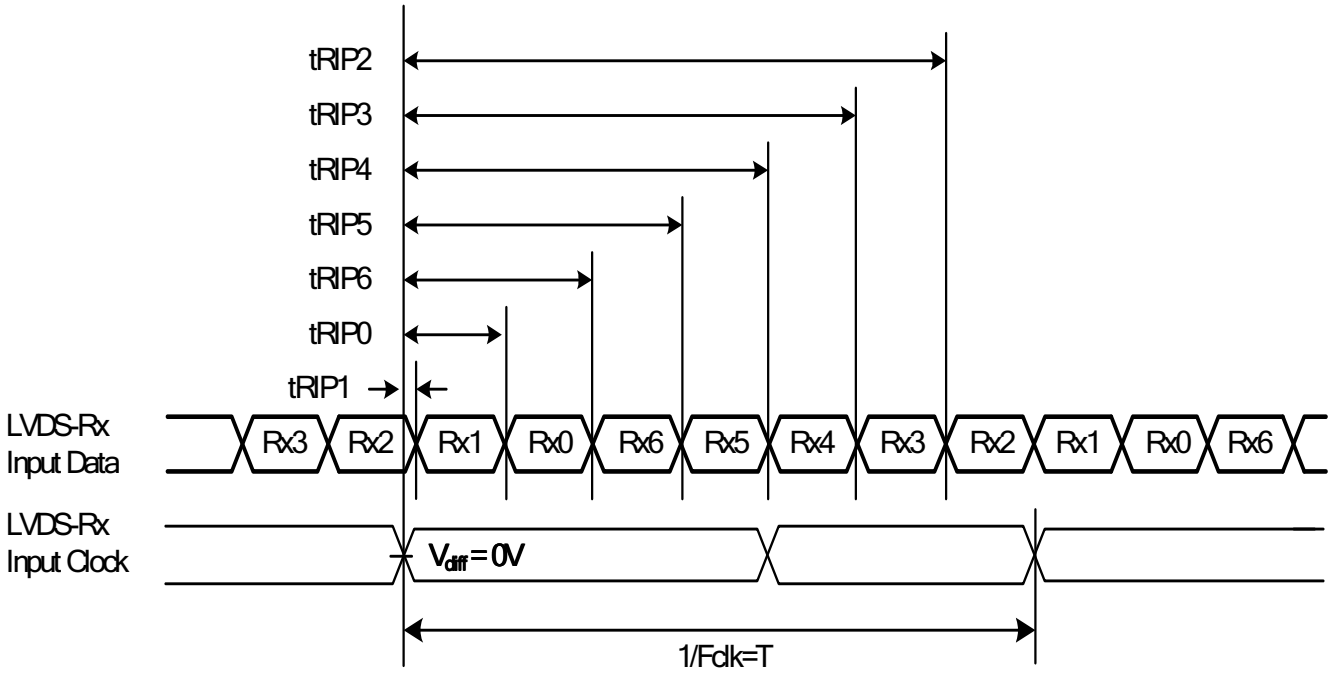
6. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min	Type	Max		
Input Clock Frequency	Fclk	Fclk (min)	--	Fclk (max)	MHz	**T=1/Fclk**
Input Data Position0	tRIP1	-|tRMG|	0	|tRMG|	ns	
Input Data Position1	tRIP0	T/7-|tRMG|	T/7	T/7+|tRMG|	ns	
Input Data Position2	tRIP6	2T/7-|tRMG|	2T/7	2T/7+|tRMG|	ns	
Input Data Position3	tRIP5	3T/7-|tRMG|	3T/7	3T/7+|tRMG|	ns	
Input Data Position4	tRIP4	4T/7-|tRMG|	4T/7	4T/7+|tRMG|	ns	
Input Data Position5	tRIP3	5T/7-|tRMG|	5T/7	5T/7+|tRMG|	ns	
Input Data Position6	tRIP2	6T/7-|tRMG|	6T/7	6T/7+|tRMG|	ns	



7. The relative humidity must not exceed 80% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.

8. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at Ta = 25±2℃]

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3.2 Interface Connections

■ LCD connector: FI-RE51S-HF (JAE, LVDS connector)

PIN	Symbol	Description	PIN	Symbol	Description
1	V_{DD}	Power Supply, +12V DC Regulated	26	CH2_0+	LVDS Channel 2, Signal 0+
2	V_{DD}	Power Supply, +12V DC Regulated	27	CH2_1-	LVDS Channel 2, Signal 1-
3	V_{DD}	Power Supply, +12V DC Regulated	28	CH2_1+	LVDS Channel 2, Signal 1+
4	V_{DD}	Power Supply, +12V DC Regulated	29	CH2_2-	LVDS Channel 2, Signal 2-
5	V_{DD}	Power Supply, +12V DC Regulated	30	CH2_2+	LVDS Channel 2, Signal 2+
6	N.C.	No connection	31	GND	Ground
7	GND	Ground	32	CH2_CLK-	LVDS Channel 2, Clock -
8	GND	Ground	33	CH2_CLK+	LVDS Channel 2, Clock +
9	GND	Ground	34	GND	Ground
10	CH1_0-	LVDS Channel 1, Signal 0-	35	CH2_3-	LVDS Channel 2, Signal 3-
11	CH1_0+	LVDS Channel 1, Signal 0+	36	CH2_3+	LVDS Channel 2, Signal 3+
12	CH1_1-	LVDS Channel 1, Signal 1-	37	CH2_4-	LVDS Channel 2, Signal 4-
13	CH1_1+	LVDS Channel 1, Signal 1+	38	CH2_4+	LVDS Channel 2, Signal 4+
14	CH1_2-	LVDS Channel 1, Signal 2-	39	GND	Ground
15	CH1_2+	LVDS Channel 1, Signal 2+	40	SCL	EEPROM Serial Clock
16	GND	Ground	41	SDA	EEPROM Serial Data
17	CH1_CLK-	LVDS Channel 1, Clock -	42	N.C.	No connection
18	CH1_CLK+	LVDS Channel 1, Clock +	43	BUS_SW	BUS_SW (SONY internal use only)
19	GND	Ground	44	Panel_SEL	reserve 0402 footprint of resistor to ground (SONY internal use only)
20	CH1_3-	LVDS Channel 1, Signal 3-	45	N.C.	No connection
21	CH1_3+	LVDS Channel 1, Signal 3+	46	SA_MODE	SA_MODE (SONY internal use only)
22	CH1_4-	LVDS Channel 1, Signal 4-	47	FLAGIN	FLAGIN (SONY internal use only)
23	CH1_4+	LVDS Channel 1, Signal 4+	48	N.C.	No connection
24	GND	Ground	49	N.C.	No connection
25	CH2_0-	LVDS Channel 2, Signal 0-	50	TCON_RDY	TCON_RDY (SONY internal use only)
			51	N.C.	AUO Internal Use Only

Note: N.C. : please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

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- LCD connector: SM08B-GHS-TB (JST)

PIN	Symbol	Description
1	V_{DD}	Power Supply, +12V DC Regulated
2	V_{DD}	Power Supply, +12V DC Regulated
3	GND	Ground
4	GND	Ground
5	GLS_CTRL	GLS_CTRL
6	GLS_CTRL2	GLS_CTRL2
7	EMI_FAIL	EMI_FAIL
8	NC	No connection

- LCD connector: 14FLT-SM2-TB (JST)

PIN	Symbol	Description
1	GPIO2	GPIO2
2	BINT	BINT
3	SPI_WP	SPI_WP
4	SPI_CS	SPI_CS
5	SPI_OUTCTRL	SPI_OUTCTRL
6	GND	Ground
7	SPI_CLK	SPI_CLK
8	GND	Ground
9	SPI_DI	SPI_DI
10	GND	Ground
11	SPI_DO	SPI_DO
12	GND	Ground
13	GND	Ground
14	GND	Ground

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- LCD connector: FH31H-56S-0.5SH(08) (HRS)

PIN	Symbol	Description	PIN	Symbol	Description
1	GND	Ground	26	3-PWM5	3-PWM5
2	1-PWM1	1-PWM1	27	3-PWM6	3-PWM6
3	1-PWM2	1-PWM2	28	3-PWM7	3-PWM7
4	1-PWM3	1-PWM3	29	3-PWM8	3-PWM8
5	1-PWM4	1-PWM4	30	GND	Ground
6	1-PWM5	1-PWM5	31	GND	Ground
7	1-PWM6	1-PWM6	32	4-PWM1	4-PWM1
8	1-PWM7	1-PWM7	33	4-PWM2	4-PWM2
9	1-PWM8	1-PWM8	34	4-PWM3	4-PWM3
10	GND	Ground	35	4-PWM4	4-PWM4
11	VLED_MPU	VLED_MPU	36	4-PWM5	4-PWM5
12	2-PWM1	2-PWM1	37	4-PWM6	4-PWM6
13	2-PWM2	2-PWM2	38	4-PWM7	4-PWM7
14	2-PWM3	2-PWM3	39	4-PWM8	4-PWM8
15	2-PWM4	2-PWM4	40	GND	Ground
16	2-PWM5	2-PWM5	41	WP_I2C	WP_I2C
17	2-PWM6	2-PWM6	42	GND_I2C	GND_I2C
18	2-PWM7	2-PWM7	43	VCC_I2C	+3.3V DC Regulated
19	2-PWM8	2-PWM8	44	SDA_I2C	SDA_I2C
20	GND	Ground	45	SCL_I2C	SCL_I2C
21	ADJ_MODE	ADJ_MODE	46	GND	Ground
22	3-PWM1	3-PWM1	47	GND	Ground
23	3-PWM2	3-PWM2	48	12V	Power Supply, +12V DC Regulated
24	3-PWM3	3-PWM3	49	12V	Power Supply, +12V DC Regulated
25	3-PWM4	3-PWM4	50	STDBY	STDBY
			51	POWER_ON	POWER_ON
			52	FAIL	FAIL
			53	REF	REF
			54	BL_TYPE1	BL_TYPE1
			55	BL_TYPE2	BL_TYPE2
			56	GND	Ground

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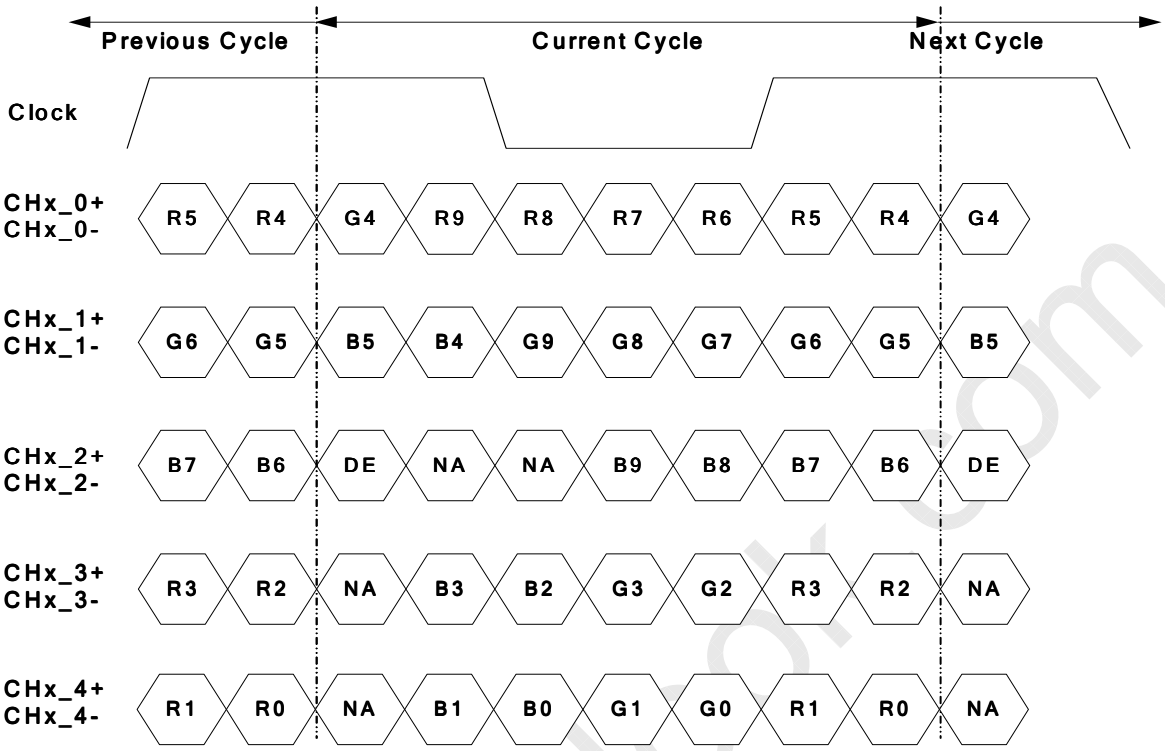
■ **LVDS ➔ JEIDA**



Note: x = 1, 2, 3, 4…

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3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table

Signal	Item	Symbol	Min	Typ.1	Typ.2	Typ.3 (typical)	Max	Unit
Vertical Section	Period	Tv	1125	1416	1360	1134	1430	Th
	Active	Tdisp (v)	1080					Th
	Blanking	Tblk (v)	45	336	280	54	350	Th
Horizontal Section	Period	Th	1092					Tclk
	Active	Tdisp (h)	960					Tclk
	Blanking	Tblk (h)	132					Tclk
Clock	Frequency	Fclk=1/Tclk	71	74.25			77	MHz
Vertical Frequency	Frequency	Fv	47	48	50	60	61	Hz
Horizontal Frequency	Frequency	Fh	65	68			71	KHz

Notes:

(1) Display position is specific by the rise of DE signal only.
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

(2)Vertical display position is specified by the rise of DE after a “Low” level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.

(3)If a period of DE “High” is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.

(4)The display position does not fit to the screen if a period of DE “High” and the effective data period do not synchronize with each other.

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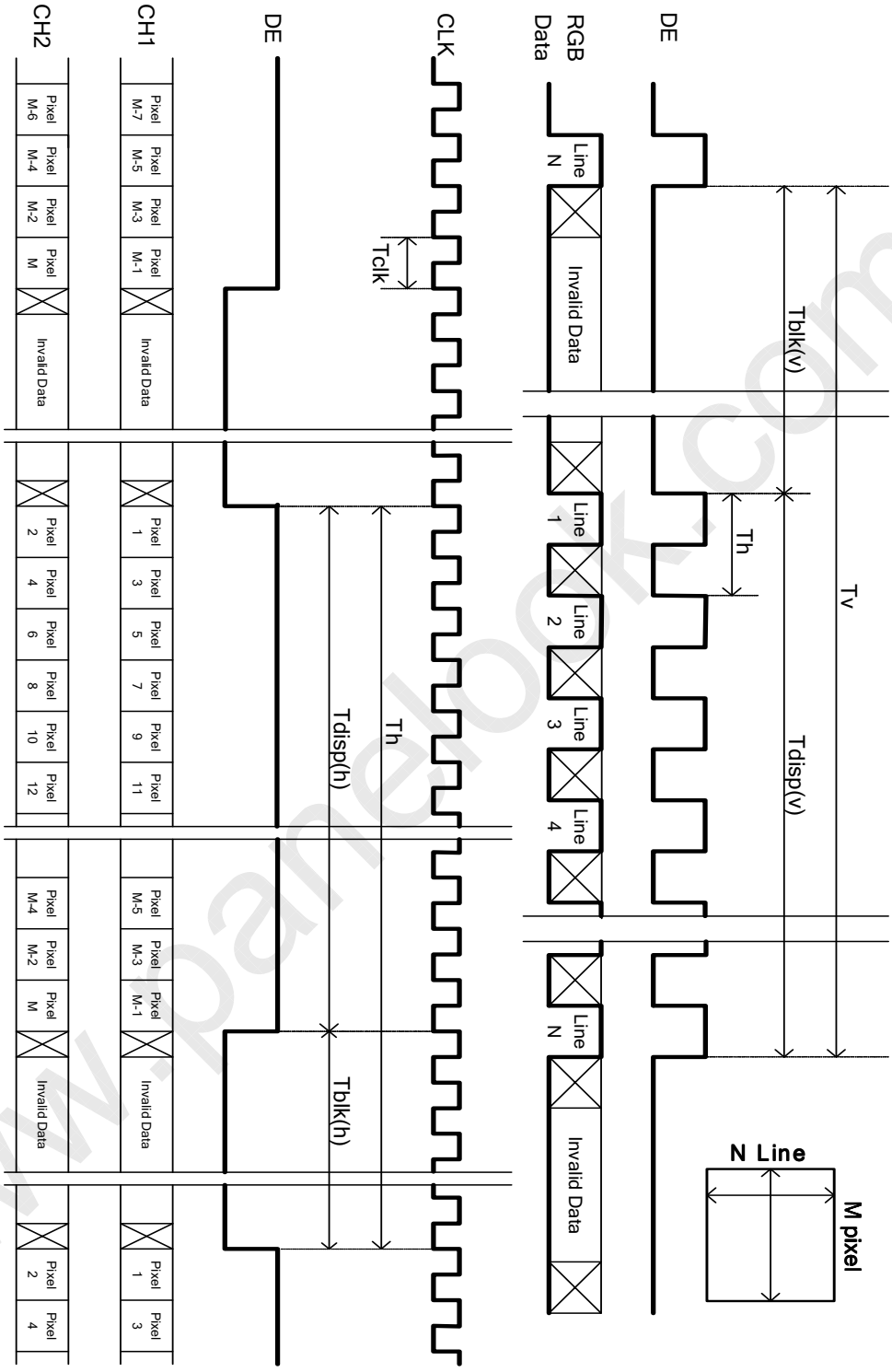
3.4 Signal Timing Waveforms



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3.5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 10 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																													
		RED										GREEN										BLUE									
		MSB									LSB	MSB									LSB	MSB									LSB
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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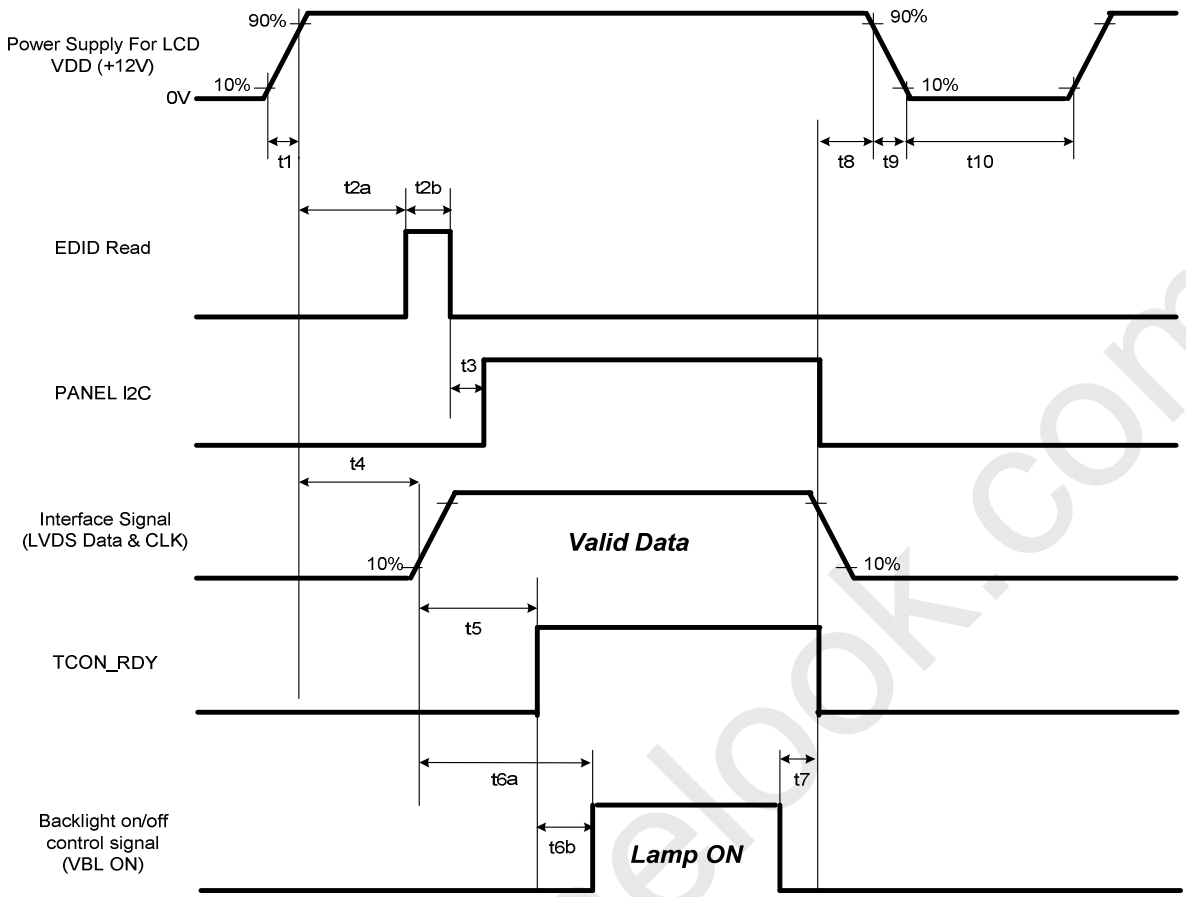
Power Sequence for LCD



Parameter	Standard[*1]			Standalone[*1]	Unit
	Min.	Type.	Max.	Type.	
t1	0.4	---	30	---	ms
t2a	10	---	100	---	ms
t2b	0[*4]	---	100[*4]	---	ms
t3	60	---	---	---	ms
t4	20	---	---	---	ms
t5	680[*4]	---	1160[*4]	---	ms
t4+t5	---	---	---	51	ms
t6a	---	---	---	---	ms
t6b	500	---	---	---	ms
t7	100[*2]	---	---	---	ms
t8	45	---	---	---	ms
t9	0[*3]	---	300[*3]	---	ms
t10	500	---	---	---	ms

Note:

(1) Standard mode is used for customer's operation. Standalone mode is used for panel factory operation.

(2) t7=0 : concern for residual pattern before BLU turn off.

(3) t9 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)

(4) t2b**, t5 and t9** : customer decides this value

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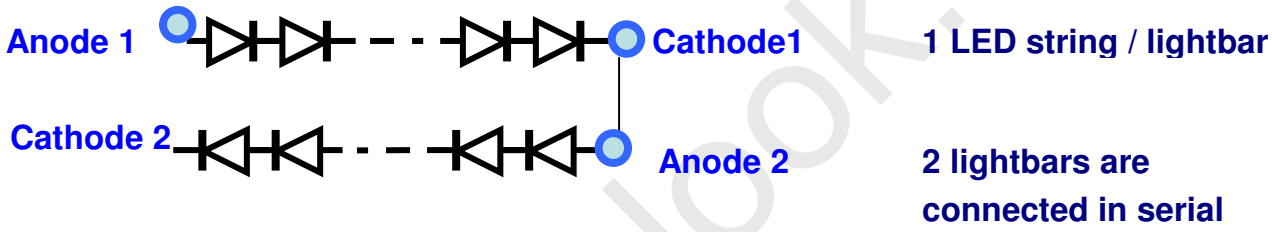
3.7 Backlight Specification

The backlight unit contains 4pcs light bar.

3.7.1 Lightbar Driving Condition

Parameter		Symbol	Values			Unit	Note
			Min	Typ	Max		
Forward Current	Anode	IF (anode)		90		mA	
(one light bar)	Cathode	IF (cathode)		90	150	mA	
Peak Forward Current		IFP			300	mA	<1msec.
Forward Voltage		VF	115.2	122.4	129.6	V	Ta=25°C
Total Power Consumption (4 light bars)		PBL	38.88	44.06	45.36	W	



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3.7.2 Input Pin Assignment

4 Pin CN: SM04B-PASS-TBT(LF)(SN)

	CN2
	4pin
1	+ IN1
2	+ IN2
3	+ IN3
4	+ IN4

5 Pin CN: SM05B-PASS-TBT(LF)(SN)

	CN3
	5pin
1	- OUT1
2	- OUT2
3	NC
4	- OUT3
5	- OUT4

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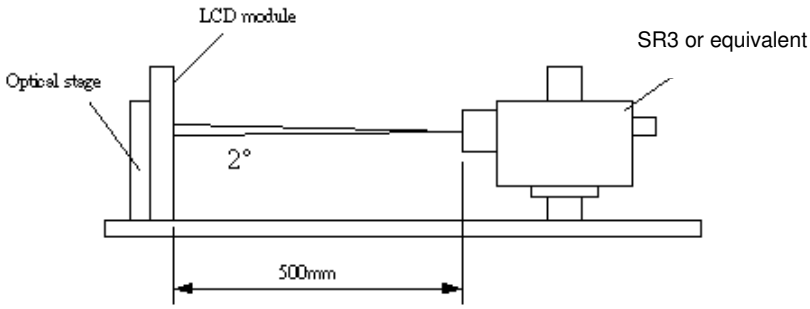
4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max		
Contrast Ratio		CR	4000	5000	--		1
Surface Luminance (White)		L_{WH}	360	450	--	cd/m^2	2
Luminance Variation		$\delta_{WHITE(9P)}$	--	--	1.33		3
Response Time (G to G)		Tγ	--	5.5	--	Ms	4
Color Gamut		NTSC		72		%	
Color Coordinates							
	Red	R_X	Typ.-0.03	0.630	Typ.+0.03		
		R_Y		0.330			
	Green	G_X		0.320			
		G_Y		0.620			
	Blue	B_X		0.150			
		B_Y		0.040			
	White	W_X		0.280			
		W_Y		0.290			
Viewing Angle							5
	x axis, right(φ=0°)	θ_r	--	89	--	degree	
	x axis, left(φ=180°)	θ_l	--	89	--	degree	
	y axis, up(φ=90°)	θ_u	--	89	--	degree	
	y axis, down (φ=270°)	θ_d	--	89	--	degree	

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Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$$

2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED current I_F = typical value (without driver board), LED input VDDB =24V, I_{DDB}. = Typical value (with driver board), L_{WH}=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location. .
3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

   $\delta_{WHITE(9P)}$= Maximum(L_{on1}, L_{on2},…,L_{on9})/ Minimum(L_{on1}, L_{on2},…L_{on9})
4. Response time T_γ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_V=120Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

T_γ is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".

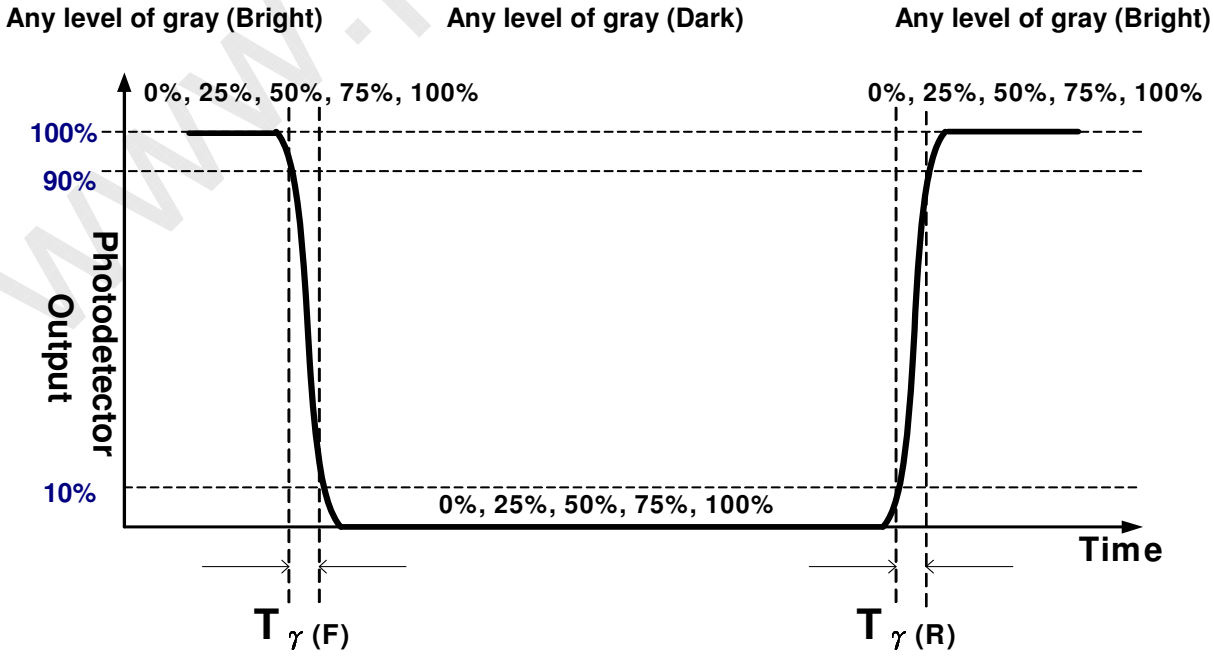

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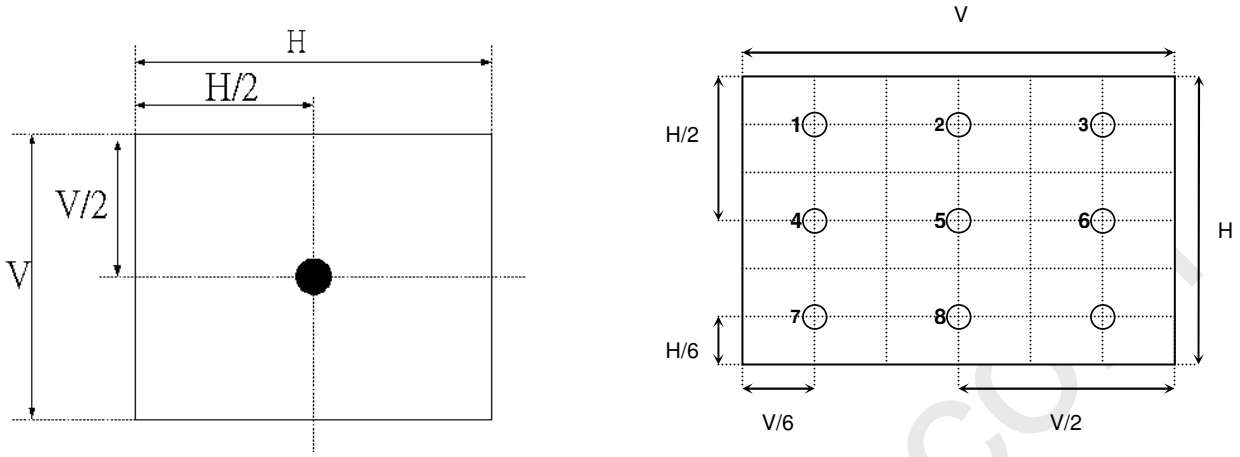
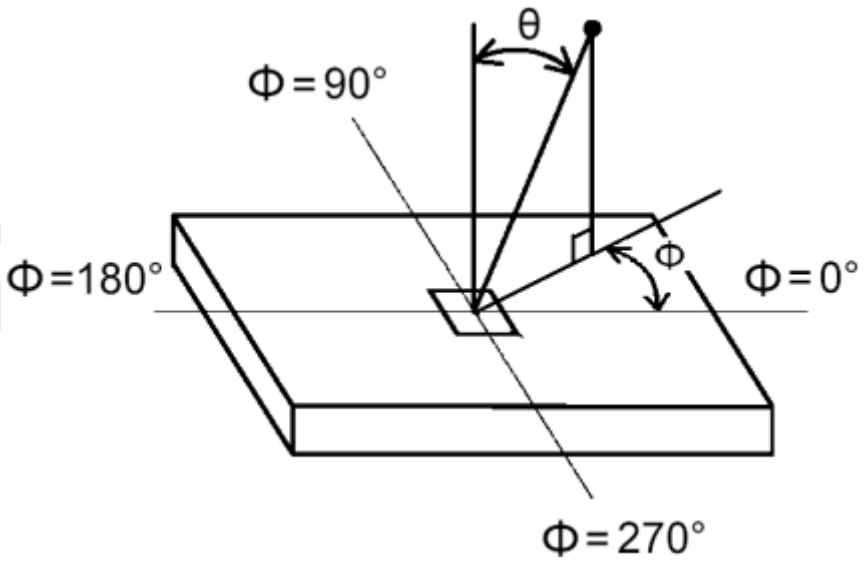
FIG. 2 Luminance



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle



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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T315HW07 V7. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	733.6.0	mm	
	Vertical	432.0	mm	
	Depth (Dmin)	10.8	mm	to rear
	Depth (Dmax)	25.1	mm	To shielding cover
Weight (Typ.)	4700		g	

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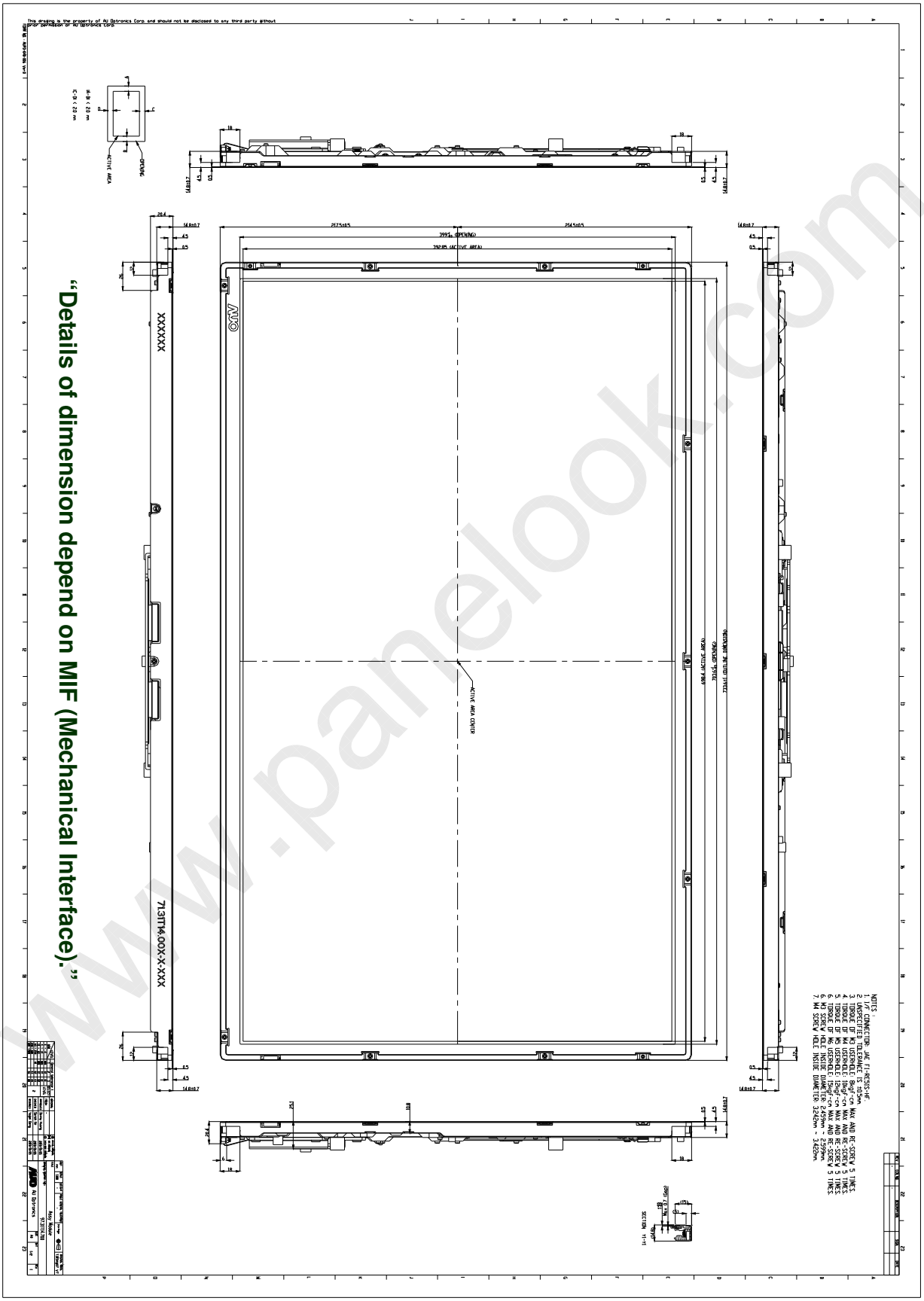
Front View



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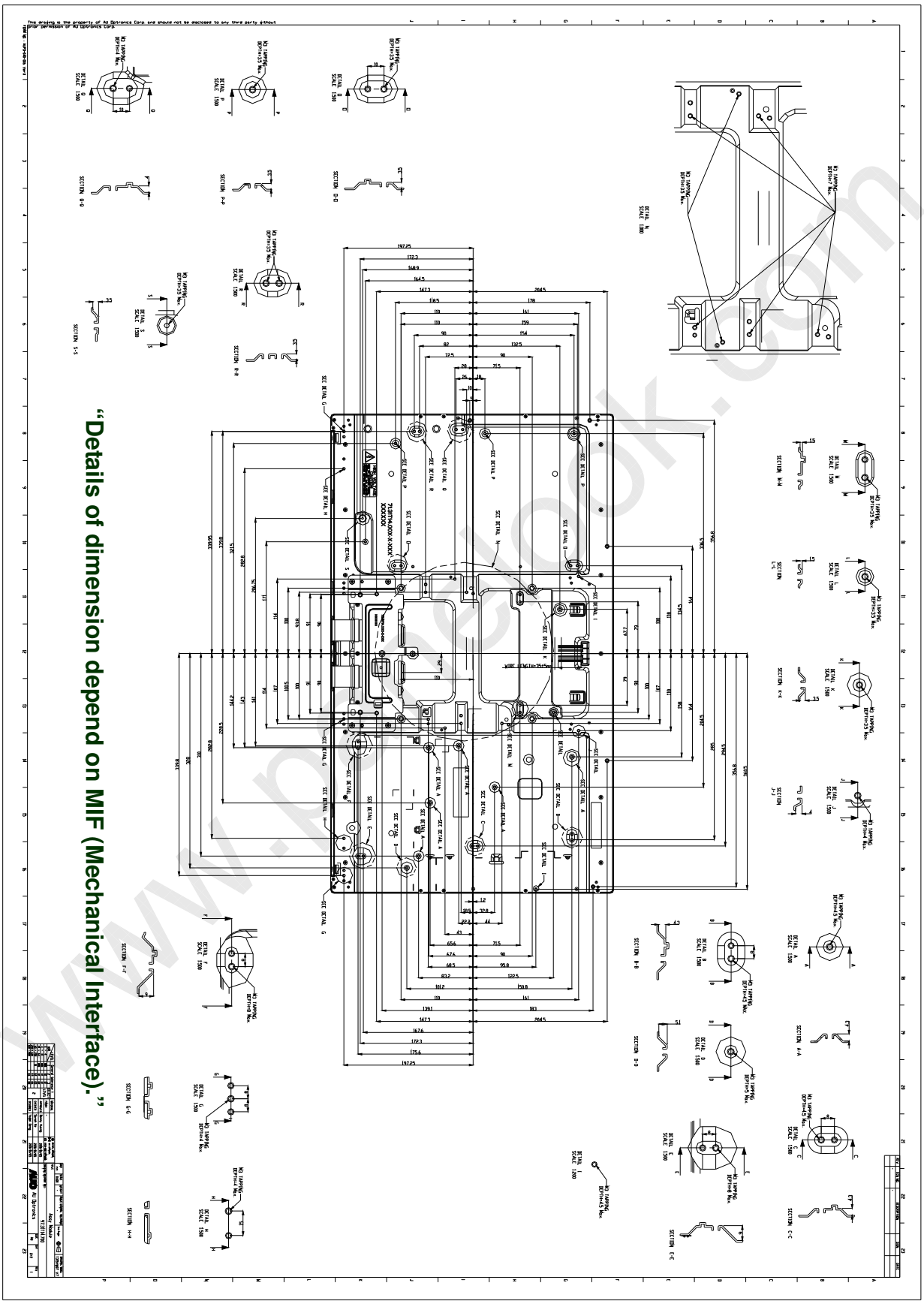
Back View



"Details of dimension depend on MIF (Mechanical Interface)."

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6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60℃, 300hrs
2	Low temperature storage test	3	-20℃, 300hrs
3	High temperature operation test	3	50℃, 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
5	Vibration test (non-operation)	3	Wave form: random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z 10min per axes X,Y,Z: Horizontal, face up
6	Shock test (non-operation)	3	Shock level 50G ,20ms ±X,Y,Z axis Waveform: half sine wave Direction: One time each direction
7	Vibration test (With carton)	5	Random wave (1.05Grms 10~200Hz) Duration : X,Y,Z 10min per axes
8	Drop test (With carton)	5	Height: 38.1cm (ASTMD4169-I) 1 corner, 3 edges, 6 surfaces (refer ASTM D 5276)

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7. International Standard

7.1 Safety

(1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) IEC 60950-1 : 2001, IEC 60065:2001 ; Standard for Safety of International Electrotechnical Commission

(3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

(1) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute(ANSI), 1992

(2) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” International Special committee on Radio Interference.

(3) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization. (CENELEC), 1998

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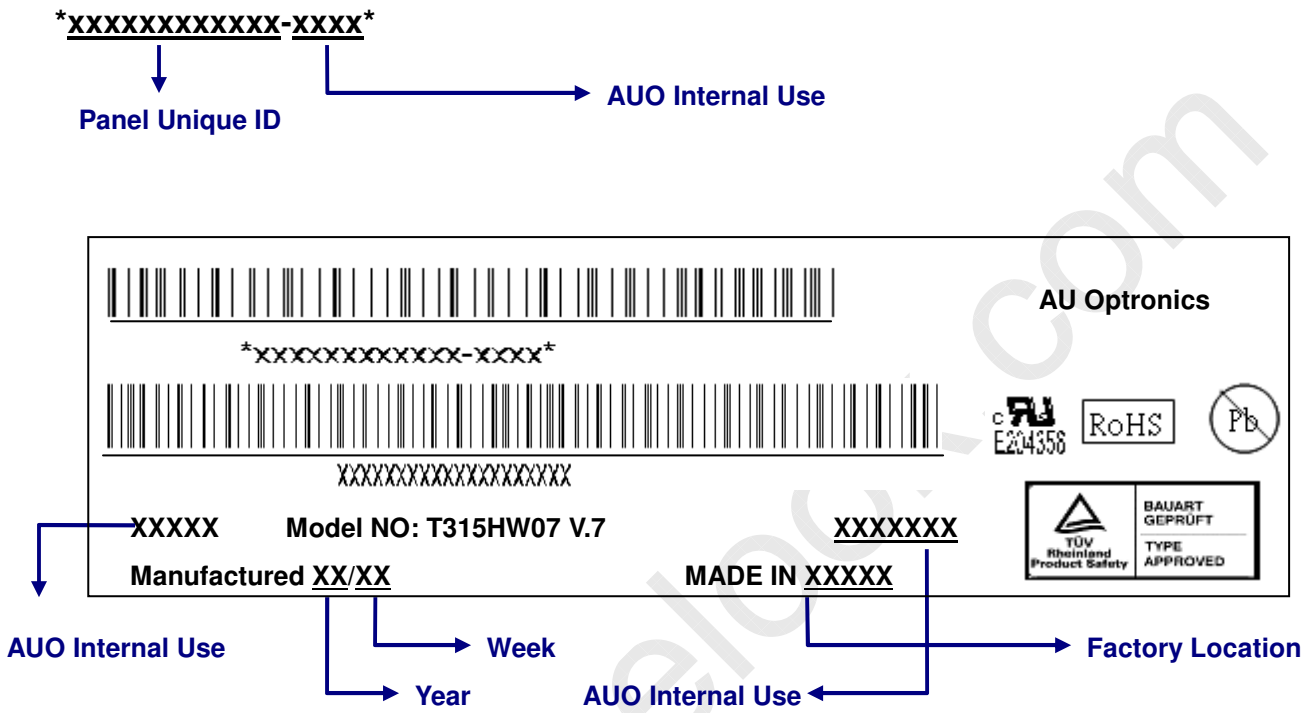
8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:



Green mark description

(1) For Pb Free Product, AUO will add Pb for identification.

(2) For RoHs compatible products, AUO will add RoHS for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:

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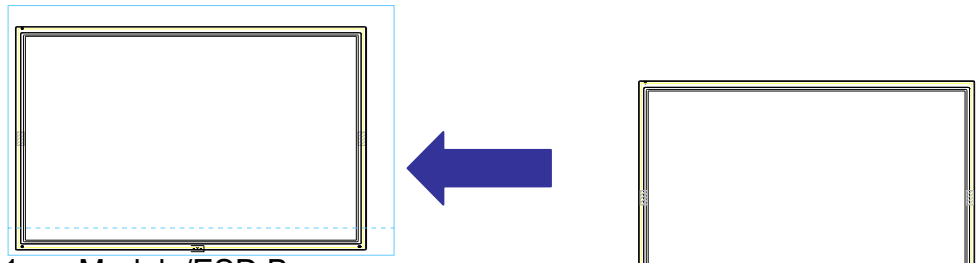
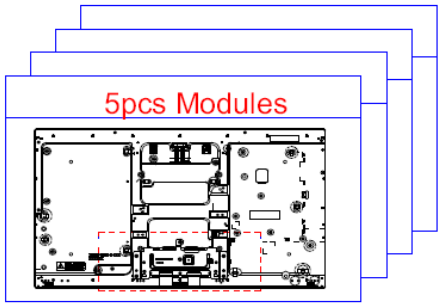
8-2 PACKING METHODS:

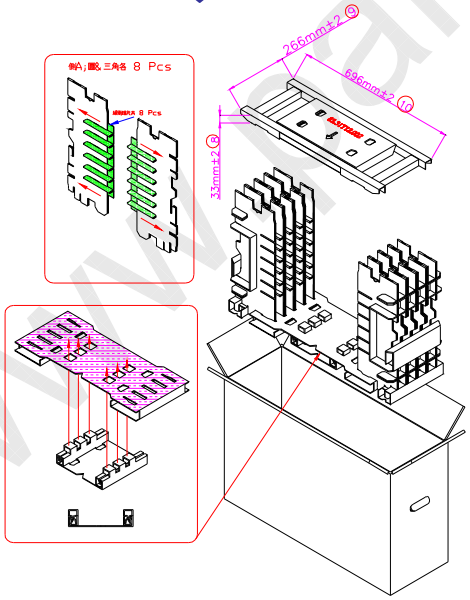


1pcs Module/ESD Bag

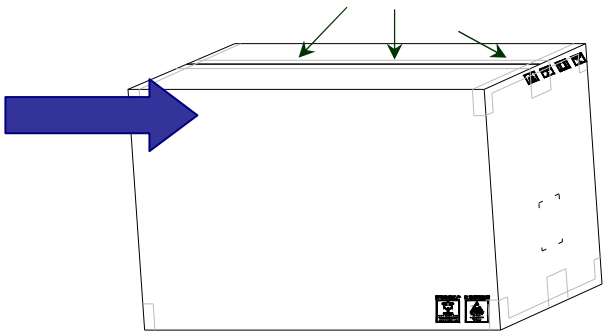


Packing Way : Panel upside down

Cushion set



" H " Tape



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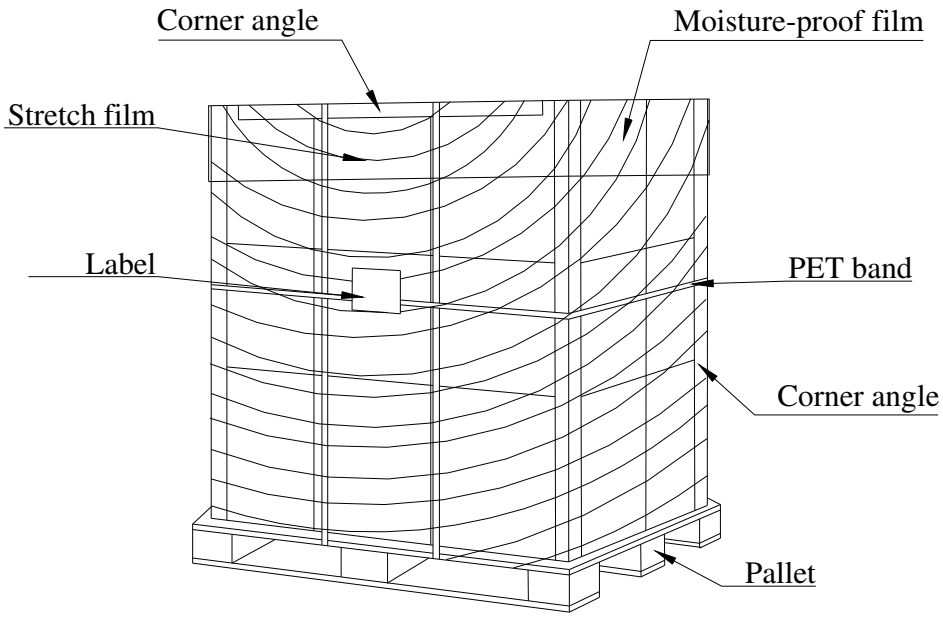
8-3 Pallet and Shipment Information

	Item	Specification			Packing Remark
		Quantity	Dimension	Weight (kg)	
1	Packing BOX	5pcs/box	828(L)mm*283(W)mm*536(H)mm	31	
2	Pallet	1	1150(L)mm*840(W)mm*132(H)mm	13	
3	Boxes per Pallet	8 boxes/Pallet			
4	Panels per Pallet	40 pcs/pallet			
5	Pallet after packing	N/A	1150(L)mm*840(W)mm*1204(H)mm	261	



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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.

(2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

(1) The device listed in the product specification sheets was designed and manufactured for TV application

(2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)

(3) Response time depends on the temperature. (In lower temperature, it becomes longer..)

(4) Brightness of CCFL depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.

(5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(6) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall

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be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.

(3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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Appendix 1

EDID Setting

Item	Description	Value
Vendor code	**Vendor Code** 0 : - 1 : AUO	**1**
Panel Inch	**Panel Inch, setting function:** 21.6inch= 22 / 26inch= 26 **32inch= 32** / 37inch= 37 40inch= 40 / 42inch= 42 46inch= 46 / 52inch= 52	**22**
H.Resolution	**Panel Horizontal resolution information.** **16bit : 0x02 = MS Byte, 0x03 = LS Byte** (1) Horizontal resolution = 3840 (2) Horizontal resolution = 1920 (3) Horizontal resolution = 1366	**1366**
V.Resolution	**Panel Vertical Resolution information:** **16bit : 0x04 = MS Byte, 0x05 = LS Byte** (1) Vertical resolution = 2160 (2) Vertical resolution = 1080 (3) Vertical resolution = 768	**768**
V. Frequency	**Panel Vertical frequency information.** 0: 50Hz / 60Hz 1: 100Hz / 120Hz 2: 200Hz / 240Hz	**0**
Data format	**Panel LVDS Data format information.** 0: 6bit / 1: 8bit 2: 10bit / 3: 12bit 4: 14bit / 5: 16bit	**1**
Part Number	**Panel maker's version information.** **@ Example: T315HW07 V7** **Item:** **(0),(1),(2),(3),(4),(5),(6),(7),(8),(9),(10),(11)** "T","3","1","5","H","W","0","7"," ","V","7"," ? "	**Capitalization**

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
T	3	1	5	H	W	0	7		V	7					

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Appendix 2

i. EMI specification

Model name: **T315HW07 V7**

Item	Min	Typ	Max	Unit
EMI level (Note)	---	---	-6	dB(μ V/m)
SSCG	---	350	---	ps

Note:
甲、Criteria: CISPR22
乙、Signal generator: PSG400 (Sony EMCS)
丙、EMI site: Sony EMCS Ichinomiya Tec. or using correlation value
丁、Find result should be checked by connecting with TV-set

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